

ABSTRACT OF THE DISCLOSURE

A liquid crystal display device that comprises a plurality of gate lines formed in parallel to each other, a plurality of source lines formed in parallel to each other and orthogonal to the gate lines, an array of cells formed in rows and columns, each of the cells being formed near an intersection of one of the gate lines and one of the source lines, a first transistor of each of the cells disposed at an N-th row and M-th column, N and M being integers, driven by an (N-2)-th gate line, and a second transistor of the each of the cells driven by an N-th gate line.

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